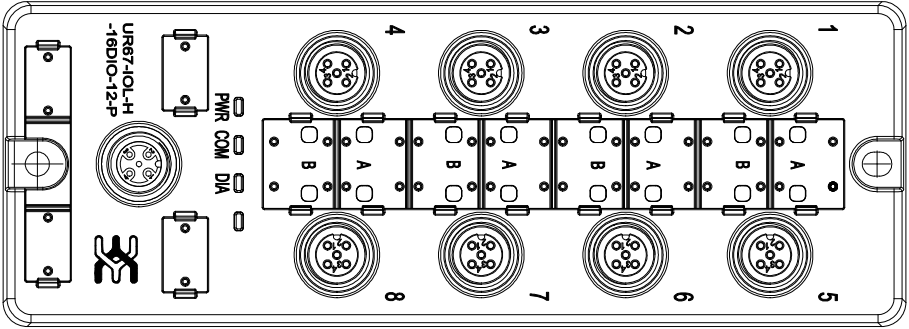




接线示意图		接口定义说明	
PNP输入 (2线制)		M12x8 I/O Pin1 Vaux Pin2 CH.B:Signal In/Out Pin3 GND Pin4 CH.A:Signal In/Out Pin5 FE	
PNP输入 (3线制)			
PNP输出			
IO-LINK		M12x1 IO-LINK Pin1 24VDC Pin2 n.c. Pin3 GND Pin4 C/Q(IO-LINK) Pin5 FE	

General Tolerances:				Tolerances ISO 8015			
Changes: PLM				PLM			
Mat. No. (SAP) 3076270000				Weidmüller			
PLM				Drawing no. Scale: .. Sheet 1 / 1			
Drawn PLM							
Responsible PLM							
Approved PLM							
9				10			
				11			
				A2			

UR67-IOL-H-16DIO-12-P